

15930 U.S. PRO
10/045889

11/07/01

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10045889	FILING DATE 11/07/2001	CLASS 710	SUBCLASS 36	GAU 2182	EXAMINER KIM, Harold
**APPLICANTS: Boike Mark; Phan Alan;					
**CONTINUING DATA VERIFIED: 404E UK, 3/18/04					
** FOREIGN APPLICATIONS VERIFIED: NONE UK, 3/18/04					
PG-PUB		DO NOT PUBLISH <input checked="" type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no		5201-25100 01-545	
Verified and Acknowledged Examiners's initials		UK			
TITLE : Method and system for a host processor to broadcast data to instruction or data memories of several processors in a multi-processor integrated circuit					
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)					

BEST AVAILABLE COPY

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawg.	Figs. Drawg.
			2
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
PREPARED FOR ISSUE			
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)